

TESTA 2019

4th International Test Standards Application Workshop

Thursday May 30th – May 31st, 2019 – Baden-Baden (Germany)

Final Technical Program

Thursday, May 30th, 2019

16:30-16:40 Opening

16:40-18:00 Keynote

Test Standards: Reuse, Compatibility and Let's Reinvent the Wheel

Theresa McLaurin, ARM

Friday, May 31st, 2019

09:00-10:00 Regular Session 1

System Test Access Management (STAM) Using the IEEE-P2654

Jan Schat - NXP, Louis Ungar – A.T.E. Solutions, Bradford Van Treuren – Consultant, Heiko Ehrenberg – Goepel Electronics

Moving towards Memory CTL (IEEE 1450.6.2-2014)

Shreyas Pramod Dixit - ARM, Khushal Gelda – ARM, Divyeshkumar Dhanjibhai Vora - ARM

10:00-10:15 Coffee Break

10:15-11:15 Regular Session 2

Structural Modelling of IEEE 1687 networks

Ahmed Ibrahim - University of Twente, Hans Kerkhoff - University of Twente

IEEE Std. P1687.1: Access to IEEE Std. 1687 via UART

Erik Larsson - Lund University, Prathamesh Murali - Lund University, Gani Kumisbek - Lund University

11:15-12:15 SHIVA/TruDevice-Panel

Secure hardware infrastructure for test and reliability

12:15-13:15 Lunch

13:15-13:45 Regular Session 3

Update on IEEE 1687 RSN benchmarks: extensions and ecosystem

Artur Jutman – Testonica Labs

13:45-15:15 Embedded Tutorial

Connecting the Dots: How IEEE P1687.1, P1687.2, and IEEE P2427 Play Together – TESTA-Version

Jeff Rearick - AMD, Martin Keim – Mentor Graphics, Vladimir Zivkovic - Cadence

15:15-15:30 Closing